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# Study of Failure Mode and Effect Analysis (FMEA) on Capacitor Bank Used in Distribution **Power Systems**

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Abstract: Failure Mode and Effect Analysis (FMEA) is the systematic procedure for the analysis and assessment of the potential failure of the equipment. Failure modes of the equipment, causes and effects of the failure modes, detection methods and mitigation methods, as well as the severity of the effects and frequency are specified in the FMEA. This study is a part of the project "consultancy services for preparation of network asset maintenance standards & associated asset management documentation" which are carried out by Monenco consulting engineering company (Iran) for Majan Electricity Company SAOC (Oman). This paper presents FMEA and related worksheets for capacitor banks used in Oman distribution power system and consist of following items: component of the equipment, functions of the component, failure modes of the component, failure causes, failure effect (local and final), detection method, compensating provision, severity of the effect and eventually frequency of the failure modes. This method can be utilized for FMEA execution of all other equipment of the power distribution system. The output of the FMEA can be utilized for critically analysis, Reliability centered maintenance (RCM) and risk based maintenance initiatives.

Keywords: Failure Mode and Effect Analysis, FMEA, Asset Management, capacitor bank.

#### I. INTRODUCTION

activities and practices through which an organization optimally and sustainably manages its assets and asset systems, their associated performance, risks and expenditures over their life cycles for achieving its organizational strategic plan [1].



Fig. 1. Various levels of implementing asset management [1]

Referring to figure 1, asset management can be utilized in four stages. In the lowest one, management of the equipment of the power system is considered. Optimizing the life cycle activities of the equipment is the objective of this level. Two upper levels i.e. manage asset systems and The increasing adoption of power electronic converters manage asset portfolio, are for the management of the such as in electric drives [3], [4] generate current entire of the power grid. The upper level is associated with harmonics which may even affect the local marginal prices

Asset Management is a systematic and coordinated the management of the whole of the corporation/ organization. This paper describes some tools, methods and activities, which can be utilized as asset management in the lowest level of the figure 1 i.e. managing assets and equipment of the power system. According to this figure, life cycle of the asset consists of four stages: Createacquire, Utilize, Maintain, and Renew/dispose. Some tools and methods can be utilized for the best implementing of the asset management program in its life cycle, such as Failure Mode and Effect Analysis, Health Index Calculation, Maintenance Standard Documentation and Asset mission document. In this paper, only Failure Mode and Effect Analysis will be studied.

> Failure Modes and Effect Analysis (FMEA) is a systematic procedure for the analysis of a system to identify the potential failure modes, their causes and effects on system performance.

> FMEA implementation is based on the "British Standard 60812:2006" [2]. Failure mode, causes, effects, detection method, mitigation method and severity are defined in table I. A work sheet (table V) is provided for capacitor banks according to table I and the remainder of this paper illustrates each column of this table.

#### **II. EQUIPMENT COMPONENT & FUNCTIONS**



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compensation and power factor correction. A capacitor insulated with a solid insulation film and insulating liquid.

in transmission level [5] or change the security unit is the building block of any shunt capacitor bank. The characteristics of the networks such as islanding detection capacitor unit is made up of individual capacitor elements, when synchronous generators are enhanced [6]. Capacitor arranged in parallel/series connected groups. Each Banks are installed to provide capacitive reactive capacitor element is an insulated foil capacitor which is

#### TABLE I FMEA WORKSHEET WITH RELATIVE DEFINITIONS

С	H			Failur	e effect		g G	
omponent	unctions	Failure modes	Failure causes	Local Effect	Final Effect	Detection method	mpensatin Provision	Severity
Constitue	Major fun	Failure n ability a	Cause or sec a process t o' Failure n ability	Consequence of a failure mode in terms of the operation, function, or status of a system.		The way in and the m maintainer	The metho effec	An assessme failure mo
ents/elements of the asset	ctions of each component	node is termination of the of an item to perform required function	quence of causes that initiat hat leads to a failure mode ver a certain time.	The effects of the failure mode on the system elemen under consideration	The impact of a possible failure on the highest system level and evaluated by the analysis of all intermediate levels	which the failure is detecte leans by which the user or is made aware of the failure	ds to prevent or reduce the ct of the failure mode	ent of the significance of th de effect on item operation

In addition, there are internal discharge resistors in the unit Upon the capacitor failure, the fuse removes the affected that reduces the unit residual voltage and allowing element only. The other elements, connected in parallel in switching the banks back after removing it from service. Figure 2 shows that a capacitor unit is contained within a higher voltage across them. Shunt capacitor banks usually metal container and connected to two voltage insulated bushings for external connections.

In Oman distribution grid, always internally fused capacitor bank is used. It means that, each capacitor element is fused inside the capacitor unit. A simplified fuse is a piece of wire sized to melt under the fault current, and encapsulated in a wrapper able to withstand the heat produced by the arc during the current interruption (Figure 2).



Fig. 2. Capacitor Unit

the same group, remain in service but with a slightly consist of multiple units in series, which are connected as double star ungrounded. Capacitor banks are metal-clad in Oman distribution grid. Other component of the capacitor banks are support insulators and interconnecting fuses.

#### **III.FAILURES, CAUSE AND EFFECT OF THE FAILURES**

Some major failure modes of capacitor banks are introduced as following [7]-[9].

#### A. Capacitor Element Short Circuit

Each capacitor element is an insulated foil capacitor which is insulated with a solid insulation film and insulating liquid. The failure mode of the capacitor element is an insulation film failure across the element foil capacitors and shorting the foil. Most of these failures are due to some cavities inside the solid insulation film that result in partial discharges in the insulation [10]. After failure, relative fuse removes the affected element only. The other elements, connected in parallel in the same group, remain in service but with a slightly higher voltage across them. Usually the unbalance relay that measure the neutral point current of the capacitor bank, doesn't trip by one element



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short circuit, But if some other elements will be shorted, I. Electrical Flashover of Support Insulators unbalance relay will trip the capacitor bank (depending on If the surrounding air is ionized/contaminated or the unbalance relay setting).

#### B. Open Circuit

Disconnection of any interconnecting wires in the capacitor unit results to change in capacitance and probability to unbalance relay trip.

#### C. Insulating Liquid Leakage

Wearing of metal enclosure of the unit, any gap or seam of the enclosure can results in insulating liquid leakage.

#### D. Bushing Degradation

Bushing degradation of unit include cracking, wear, contamination, etc. can be occurred due to external contamination, moisture, insulation failure, flash over, short circuit, probability of explosion and fire.

#### E. Fuse Failure

Fuse degradation results primarily from the failure of seals and ingress of moisture, or environment high temperature. • In short circuit situation of a capacitor element, if relative fuse is not operated, all elements in parallel in same group • will be shorted. The remaining series capacitor elements in the unit remain in service with a higher voltage across each of them. It changes the capacitance value and can result in tripping unbalance relay. On the other hand, if a fuse is blown in normal condition (no short circuit), the capacitor element will be opened and results in changing the capacitance value and probability of tripping unbalance relay (depending on unbalance relay setting but usually does not trip by one fuse mal-operation).

#### F. Thermal Stresses

Thermal stresses are due to high temperature gradient [11, 12]. Environment high temperature results in degradation of insulation in capacitor units and external insulators.

#### G. Electrical Stresses

During service, capacitor banks experience steady state, transient and dynamic over-voltage conditions (for example connecting the capacitor bank to grid when load is low, can result in overvoltage of capacitor bank).

Switching devices that re-strike during de-energizing impose additional stress on capacitors. Such stress results in cumulative and non-reversible degradation of insulation in capacitor units and external insulators. Degradation rates depend on design, safety margins, system conditions and environmental factors. The electrical surges could have detrimental effects on the capacitors structure [13].

#### H. Fail to Provide Safety in Operation

There is some interlock for correct operation of capacitor banks. If these interlock fails due to disconnecting any DC supply, wires, defect of auxiliary relay, etc., it could result to hazards for personnel or capacitor bank.

electrical over voltages are very high, flash over across the insulator and between phases can occur, which lead to short circuit. Such internal arcs can increase the pressure inside the capacitor panel and cause its burst, which can be dangerous for operators.

J. Leakage current for support insulators

Contamination and tracks over the insulator surface will cause to leakage current.

K. Thermal and electrical forces for support insulators

Short circuit currents will produce high forces that act on the insulators. In rare cases, ultra-high forces can cause failure of the insulator. However there are a few methods that can limit the amount of fault current with help of PTC devices [14].

In addition, following failure modes maybe occurred for panel of the capacitor bank.

- Misaligned and failure of mounting •
- Degradation of panel body plate (Corrosion, damage, fatigue, rust spots, graffiti, scratch, etc.)
- Failure of protection degree of panel (IP)
- Failure of lighting / heater / thermostat of panel
- Failure of door to operate properly (open, close, lock)
- Disconnection of electrical connections and/or wiring

Specifications of above panel failure mode are specified in worksheet (See table V). Capacitor structure affects the damage development caused by any current flashover [15-16].

#### **IV.DETECTION METHODS & FAILURE COMPENSATION PROVISIONS**

The detection method for each failure mode is the way in which the failure is detected and the means by which the user or maintainer is made aware of the failure. Condition monitoring activities/detection methods are specified in the worksheet (table V). In addition to the traditional detection methods, novel non-destructive testing and Evaluation (NDT&E) techniques [17] are recently introduced such as wave propagation based SHM can be employed to detect possible damages in panel body plate [18-19].

Compensation provisions are activities that prevent or reduce the effect of failure mode. These provisions are specified in the worksheet (table V).

#### V. SEVERITY CLASSIFICATION

Severity is an assessment of the significance of the failure effects (final effect) on system operation and personal safety. Severity of each failure modes is specified in the worksheet (table V) based on table II and classified to four levels; Catastrophic, Critical, Marginal and Insignificant.



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#### VI.CONCLUSION

TABLE IIIII FREQUENCY QUALITATIVE CLASSIFICATION

Capacitor banks are used in distribution networks extensively and records of Majan Electricity Company SAOC (Oman) show that some of Medium Voltage grid outage is related to capacitor banks. In the first step of this paper, capacitor bank components and functions were described. Then failure modes comprising short-circuit, open circuit. Insulating liquid leakage. Fuse failure, etc and their effects on the system operation and personal safety were explained. After that, detection methods for each failure mechanism and compensating provisions to prevent or reduce the effects of each failure were mentioned to summarize in the worksheet (table V). Then, failure mechanisms were classified in accordance with their severity. Finally, one worksheet that included all items described in this paper is produced and developed for capacitor banks used in Oman Distribution Power System. (See table V)

It must be mentioned that the output of the FMEA can be utilized for FMECA (Failure mode, effect and critically analysis). In this regard, a RPN (Risk Priority Number) is dedicated to each failure mode.

RPN = Value of (Severity) \* Value of (Occurrence)\* Value of (Detection)

where value of Severity is obtained based on the table II and value of detection and occurrence are obtained based on the following tables.

The output of the FMEA and FMECA is suitable to support RCM (Reliability Centered Maintenance) and Risk Basked Maintenance initiatives.

TABLE III SEVERITY QUALITATIVE CLASSIFICATION

Class	Severity level	Criteria
IV	Catastrophic	A failure mode which could potentially result in the failure of the system's primary functions and therefore causes serious damage to the system and its environment and/or personal injury.
ш	Critical	A failure mode which could potentially result in the failure of the system's primary functions and therefore causes considerable damage to the system and its environment, but which does not constitute a serious threat to life or injury.
Π	Marginal	A failure mode, which could potentially degrade system performance, function(s) without appreciable damage to system or threat to life or injury.
Ι	Insignificant	A failure mode, which could potentially degrade the system's functions but will cause no damage to the system and does not constitute a threat to life or injury.

Value	Class	Frequently level	Criteria (Pi: Probability of occurrence)
5	А	Frequent	$Pi \ge 0.2$
4	В	Probable	$0.1 \le \text{Pi} < 0.2$
3	С	Occasional	$0.01 \le Pi < 0.1$
2	D	Remote	$0.001 \le Pi < 0.01$
1	Е	Improbable	$0 \le Pi < 0.001$

#### TABLE IV DETECTION CLASSIFICATION

Value	Class	Criteria		
1	F	Good identification		
2	E	Fair identification		
3	D	Good detection & rough		
		identification		
4	С	Fair detection		
5	В	Rough detection		
6	Α	Complementary test		

#### REFERENCES

- [1] "PAS 55 Asset Management", published by BSI, 2008
- [2] IEC 60812, "Analysis technique for system reliability procedure for failure mode and effects analysis (FMEA)", Second edition, 2006.
- [3] S Jafarishiadeh, M Ardebili, A Nazari Marashi, "Investigation of pole and slot numbers in axial-flux pm bldc motors with singlelayer windings for electric vehicles," 24th Iranian Conference on Electrical Engineering (ICEE), pp. 1444-1448, 2016.
- [4] Seyyedmahdi Jafari Shiadeh, Mohammad Ardebili, and Parvin Moamaei, "Three-dimensional finite-element-model investigation of axial-flux PM BLDC machines with similar pole and slot combination for electric vehicles", In: Proceedings of Power and Energy Conference, Illinois, pp. 1–4, 2015.
- [5] H. Norouzi, S. Abedi, R. Jamalzadeh, M. Ghiasi Rad, S.H. Hosseinian, "Modeling and investigation of harmonic losses in optimal power flow and power system locational marginal pricing," Energy Journal, Science Direct, P-68 (2014) 140e147
- [6] H. Jouybari Moghaddam, S.H. Hosseinian, B. Vahidi, M. Ghiasi Rad, "Smart Control mode selection for proper operation of synchronous distributed generators", 2nd Iranian Conference on Smart Grids, Iran (Islamic Republic of), 2012
- [7] B. Kaztenny, G. Brunello, B. Kasztenny, C. Wester: "Shunt Capacitor Bank Fundamentals and Protection", Conference for Protective Relay Engineers-Texas A&M University, 2003.
- [8] M.A. El-Hadidy, D.H. Helmi: "Failure Analysis of Medium Voltage Capacitor Banks: The Egyptian Experience", CIRED 2009.
- [9] "Transmission Baseline Study Report", Prepared for British Columbia Transmission Corporation by Acres International Ltd, Apr. 2005.
- [10] Sarfi, V. and V. Hemmati. "Simulation of partial discharge in closely coupled cavities embedded in solid dielectrics by finite element method." In High Voltage Engineering and Application (ICHVE), 2014 International Conference on, pp. 1-4. IEEE, 2014. doi: 10.1109/ICHVE.2014.7035406
- [11] M. Masoomi, N. Shamsaei, X. Gao, S. M. Thompson, A. Elwany, L. Bian, N. Shamsaei, L. Bian, and A. Elwany, "Modeling, simulation and experimental validation of heat transfer during selective laser melting," in ASME 2015 International Mechanical Engineering Congress & Exposition, 2015.



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ISO 3297:2007 Certified

#### Vol. 5, Issue 2, February 2017

- Bian, "An Experimental-Numerical Investigation of Heat Transfer during Selective Laser Melting," in 26th International Solid Freeform Fabrication Symposium, 2015.
- [13] P. Gharghabi, J. Lee, M. S. Mazzola, and T. E. Lacy Jr., "Development of an Experimental Setup to Analyze Carbon/Epoxy Composite Subjected to Current Impulses," Am. Soc. Compos. Thirty-First Tech. Conf., 2016.
- [14] Sarfi, V., V. Hemmati, and M. M. Arabshahi. "Simulation of PTC devices as fault current limiters in power systems by finite element method." In High Voltage Engineering and Application (ICHVE), 2014 International Conference on, pp. 1-4. IEEE, 2014. doi: 10.1109/ICHVE.2014.7035503
- [15] P. Gharghabi, P. Dordizadeh-Basirabad, and K. Niayesh, "Impact of Metal Thickness and Field Shaper on the Time-varying Processes during Impulse Electromagnetic Forming in Tubular Geometries," J. Korean Phys. Soc., vol. 59, pp. 3560-3566, 2011.

- [12] M. Masoomi, S. M. Thompson, N. Shamsaei, A. Elwany, and L. [16] P. Dordizadeh-Basirabad, P. Gharghabi, and K. Niayesh, "Dynamic Analysis of a Fast-acting Circuit Breaker (Thompson) Drive Mechanism," J. Korean Phys. Soc., vol. 59, pp. 3547-3554, 2011.
  - [17] R. Jalilzadeh Hamidi, S. H. Hosseinian, and S. H. H. Sadeghi, and Z. Qu, "A novel approach to utilize PLC to detect corroded and eroded segments of power transmission lines," IEEE Trans. Power Delivery, Vol. 30, No. 2, pp. 746-754, 2015.
  - Khalili A., Samaratunga D., Jha R., Lacy T. E., Gopalakrishnan S. [18] WSFE-based User-Defined Elements in ABAQUS for Modeling 2D Laminated Composites with Complex Features" 30th ASC Technical Conference, Michigan State University, East Lansing, MI, US, 28-30 September 2015.
  - [19] Khalili A., Samaratunga D., Jha R., Lacy T. E., Gopalakrishnan S. "Wavelet Spectral Finite Element Based User-Defined Element in Abaqus for Modeling Delamination in Composite Beams" 23rd AIAA/ASME/AHS Adaptive Structures Conference, Kissimmee, FL, US, 5-9 January 2015.

ne	Fui	Failure	Enilura anusas	Failure effect		Detection	Compensating	Sev
npo ent	ncti ns	modes	Fanure causes	Local Effect	Final Effect	method	Provision	verit y
Capacitor unit	Provide capacitance	Short circuit	A failure in the capacitor element dielectric causes the foils to weld together	Blown relative fuse / Change in capacitance / Slightly higher voltage across other series capacitors	Probability of unbalance relay trip	Measuring capacitance value of unit from bushing	Replace capacitor unit after unbalance relay trip / repair unit and change short element	Π
		Open circuit	Disconnection of any interconnecting wires	Change in			Replace capacitor unit after unbalance relay trip / repair unit and connect any disconnection	Π
		Fuse failure	Failure of seals and ingress of moisture / environment high temperature	capacitance			Replace capacitor unit after unbalance relay trip / repair unit and change failure fuses	Π
		Insulating liquid leakage	Wearing of metal enclosure of the unit, any gap or seam of the enclosure	Decreasing insulator liquid	Probability of change in capacitance	Visual check	Use adhesive material / soldering	Π
		Bushing degradatio n (include cracking, wear & contaminat ion)	Insulation failure / external contamination / moisture	Partial discharge on the external surface of insulation / flashover	short circuit / personal safety / enclosure rupture or explosion	Power factor of bushing / tan delta / Visual check	Routine inspection such as cleaning / replace bushing	Π
		Thermal stresses	Environment high temperature	Cumulative and non-reversible degradation of	Degradation of the capacitor unit	Check the environment temperature	Replace capacitor unit with one with high permissible temperature	Ш
	-	Electrical stresses	Steady state, transient and dynamic over voltage condition	insulation in capacitor units and external insulators		Test insulation (Partial Discharge, tan delta) / Check records	Replace capacitor unit	III
		Fail to provide safety in operation	Interlock failure	Operation without checking the interlock	Over voltage / Damage of capacitor bank / personal safety	Check control supply supervision alarm	Check control wiring by buzzer test, find problem and fix it	IV

#### TABLE V FMEA WORKSHEET PROVIDED FOR CAPACITOR BANKS USED IN OMAN DISTRIBUTION POWER SYSTEM



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Co:	Fu o	Failure	<b>D</b> "	Failure of	effect	Detection	Compensating	Sev
mpo ent	ncti ns	modes	Failure causes	Local Effect	Final Effect	method	Provision	verit y
	Withstan	Electrical flash over	Ionized air/ contaminated air / electrical over voltages	Flash over / short circuit	Equipment damage/ hazard to personnel	Partial discharge/ Power system disturbance recorder	Visual inspection/ cleaning / partial discharge	IV
upport insulate	ding voltage and forces	Leakage current	Contaminated surface / Tracks	Leakage current	Short circuit/	visual inspection/ Partial	Visual inspection/ cleaning /partial discharge/ thermograph inspection	III
or		Thermal effects and electromec hanical forces	Excessive currents	Insulator Damage	damage	discharge/ thermograph inspection	Periodic review of system fault levels/ Power system disturbance recorder	III
	Enclose capacitor bank	misaligned and failure of mounting	Panel misplacement / displacement	Stress on insulator and bushings and other capacitor components	Failure of insulator, bushings and other capacitor components		Re-install the panel	Π
Panel		Degradatio n of panel body plate (Corrosion , damage, fatigue, rust spots, graffiti & scratch)	Impact, degradation over the time, environment condition (such as salt spray in coastal areas), etc.	Failure of body of panel / Plate corrosion and degradation / failure of IP	Probability of damage of internal component	Visual inspection	Routine maintenance / replace the panel	п
		Failure of protection degree of panel (IP)	degradation over the time, degradation or loosening sealed materials and degradation of seal of cable entrances	Ingress of dust and water	of panel		Routine maintenance	п
		Failure of lighting / heater / thermostat of panel (if exist)	Degradation over the time / impact / failure of relative circuit (disconnection, loosening, burning)	Difficulty of personnel for any necessary operation or inspection Heater / thermostat / Moisture	Personnel safety	Visual inspection Functional test	Routine maintenance /	П
		Failure of door to operate properly (open, close, lock)	Degradation over the time / panel displacement / impact	ingress Difficulty of personnel / failure of IP	Probability of damage of internal component of panel	Visual inspection / test	replace the panel	П
		Disconnect ion of electrical connection s or wiring	t Impact / loosening of connection / design and installation g and installation g capa bar		Disconnectin g capacitor bank	Functional test / buzzer test	Find disconnection and fix it	Π